



Fig. 1

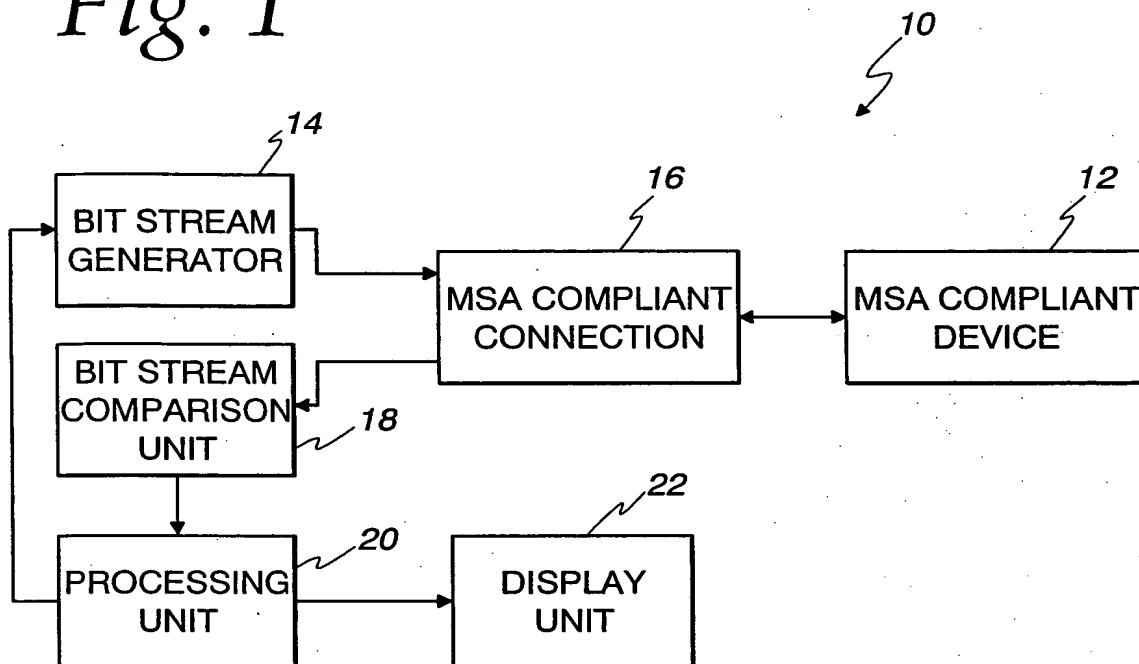


Fig. 2

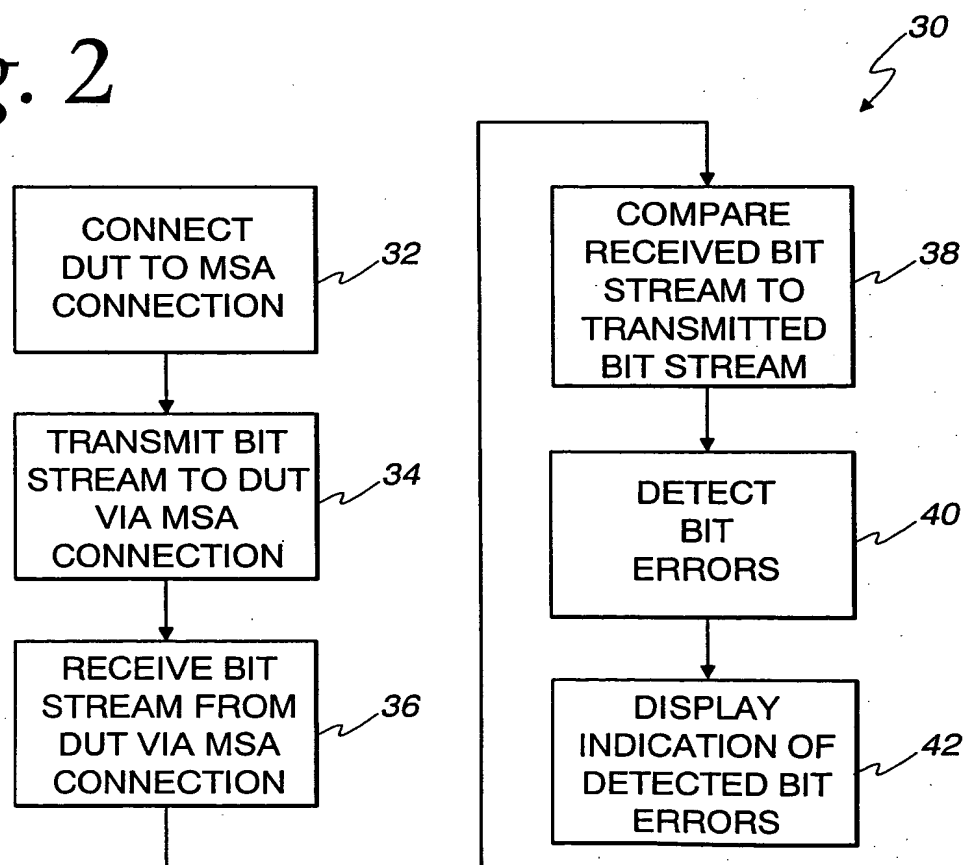
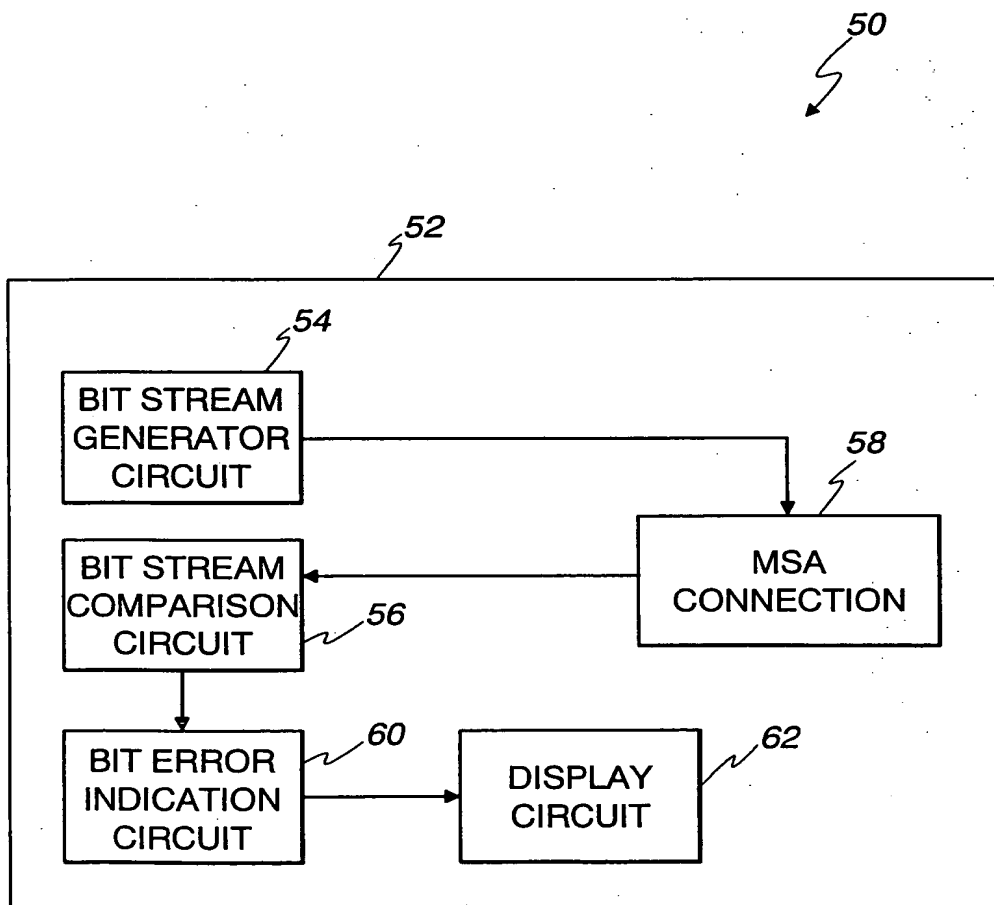




Fig. 3





Serial No.: 10/027,053
 Title: Bit Error Rate Test System for Multi-Source Agreement Compliant Transceivers
 Inventors: Rose et al.
 Sheet 3 of 4 (fig. 4a)

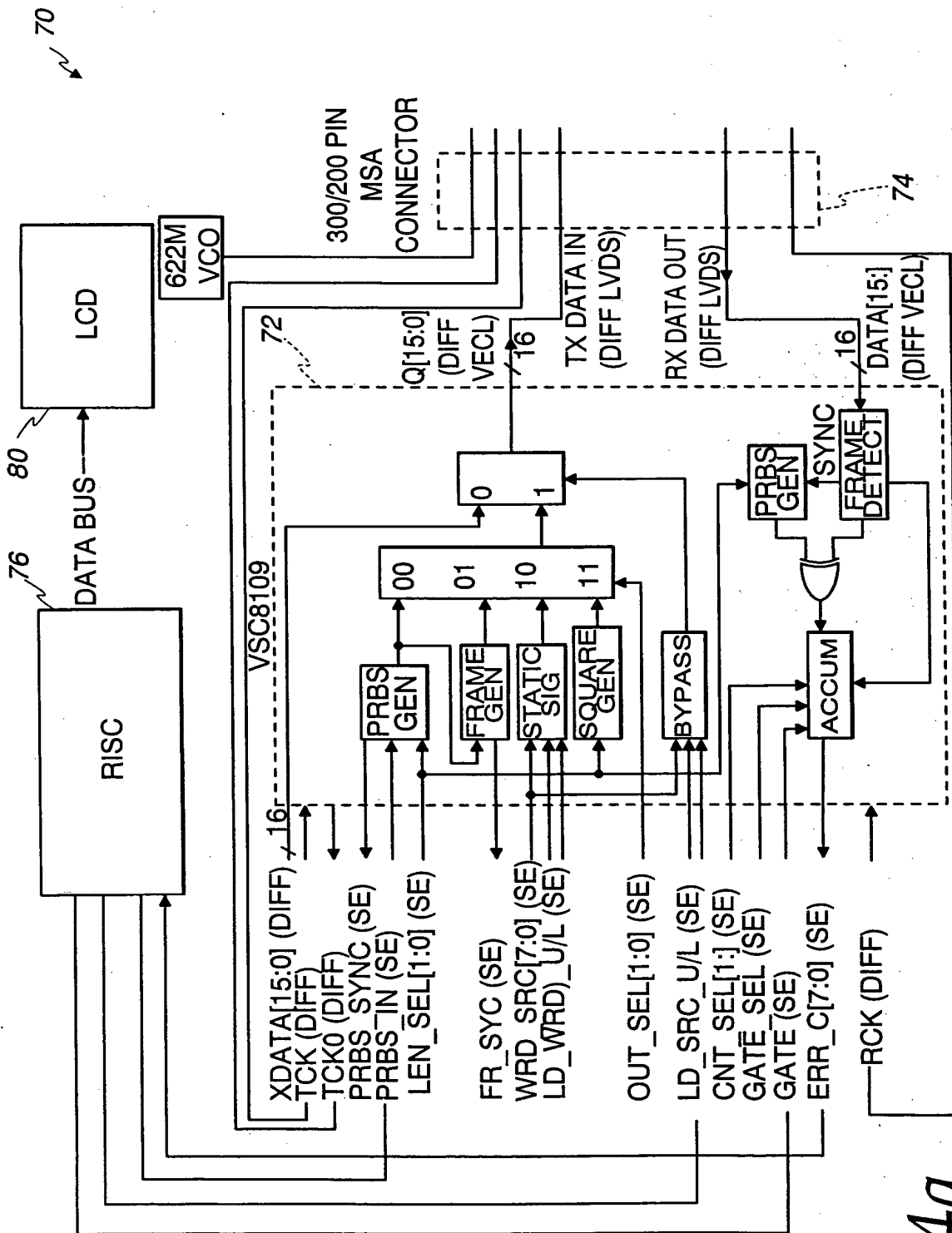


Fig. 4a



Serial No.: 10/027,053
 Title: Bit Error Rate Test System for Multi-Source Agreement Compliant Transceivers
 Inventors: Rose et al.
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Fig. 4b

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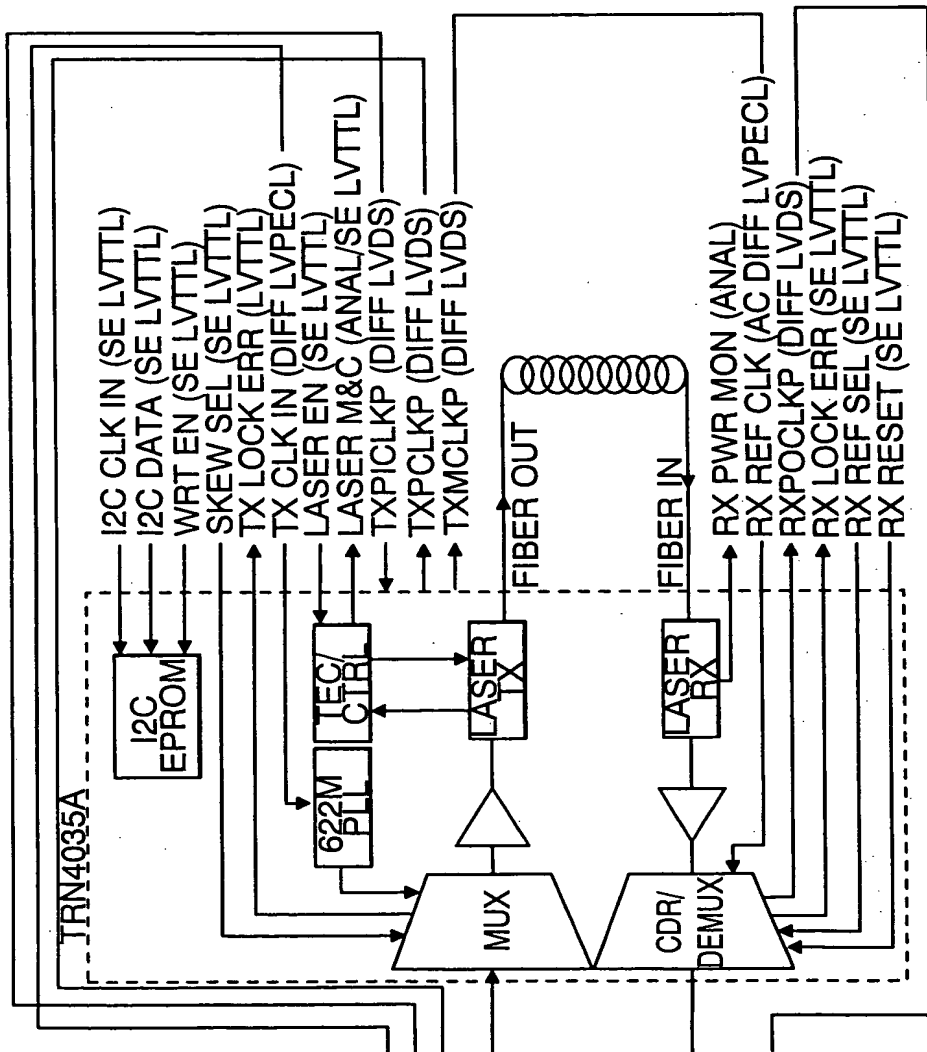


Fig. 4

Fig. 4a

Fig. 4b